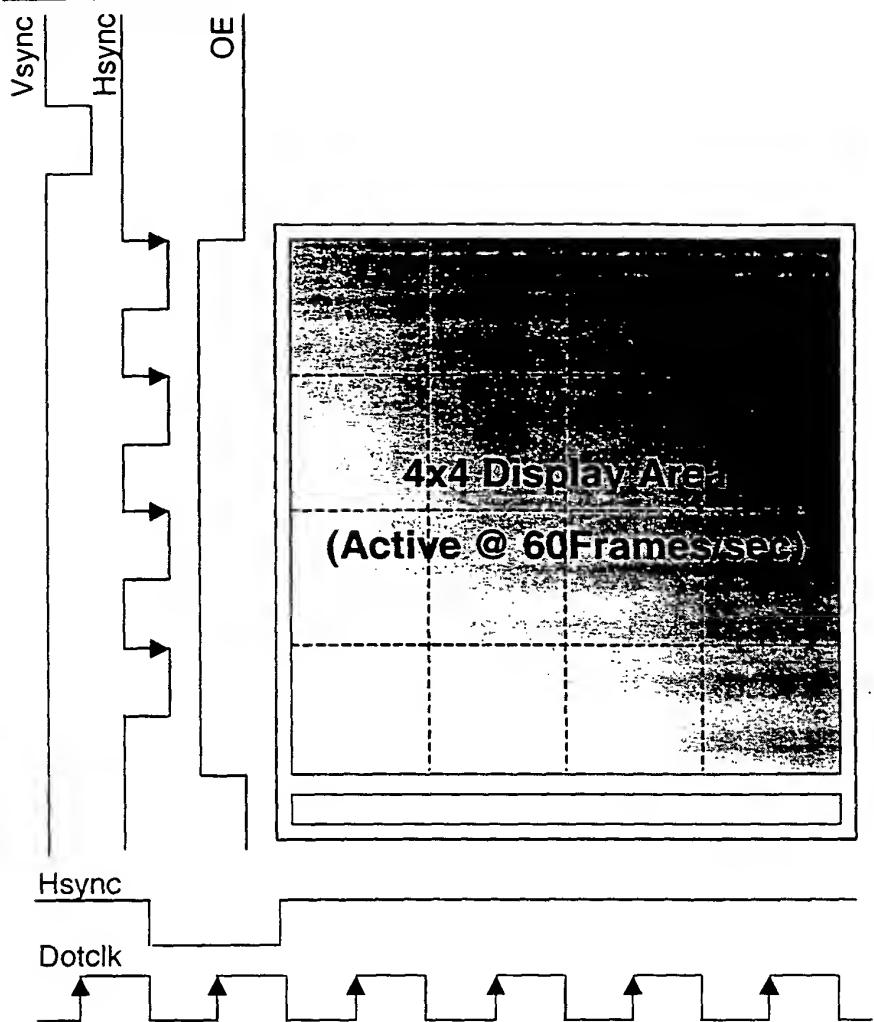


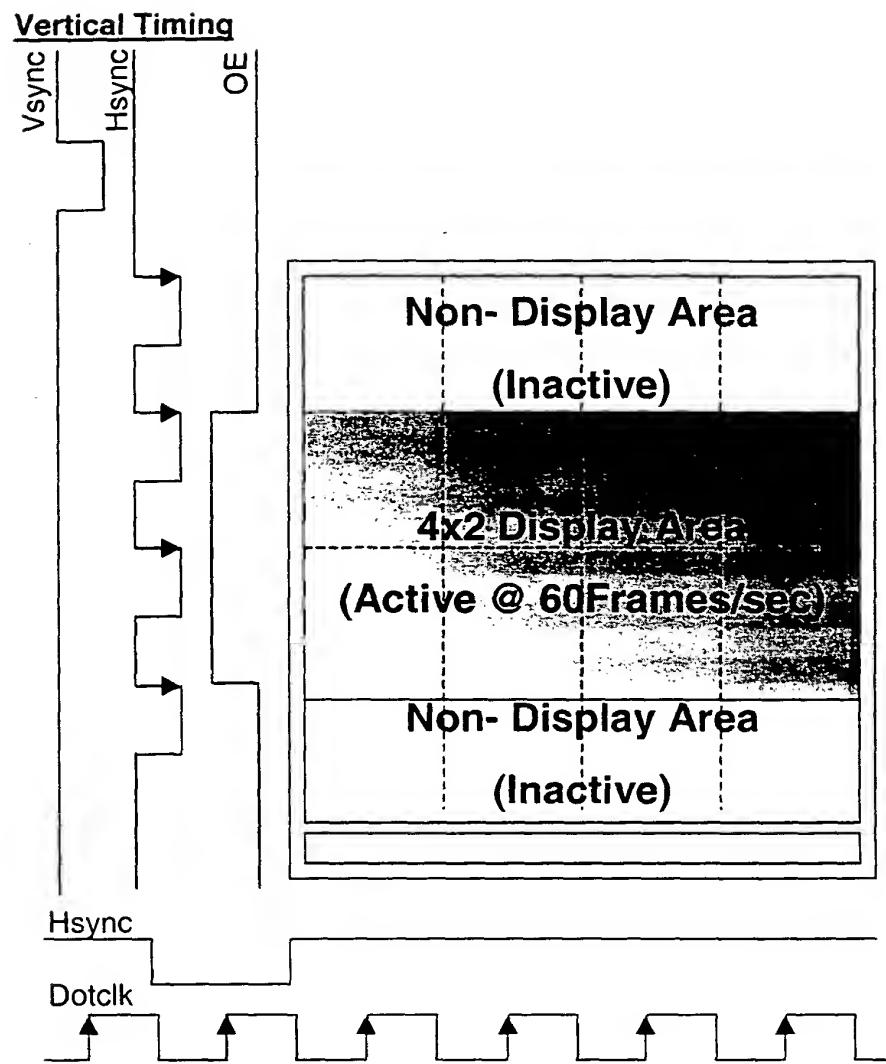
Vertical Timing



Horizontal Timing

(PRIOR ART) **Fig 1**

C322497RA-FCC



(PR. ON A. 2) **Fig 2**

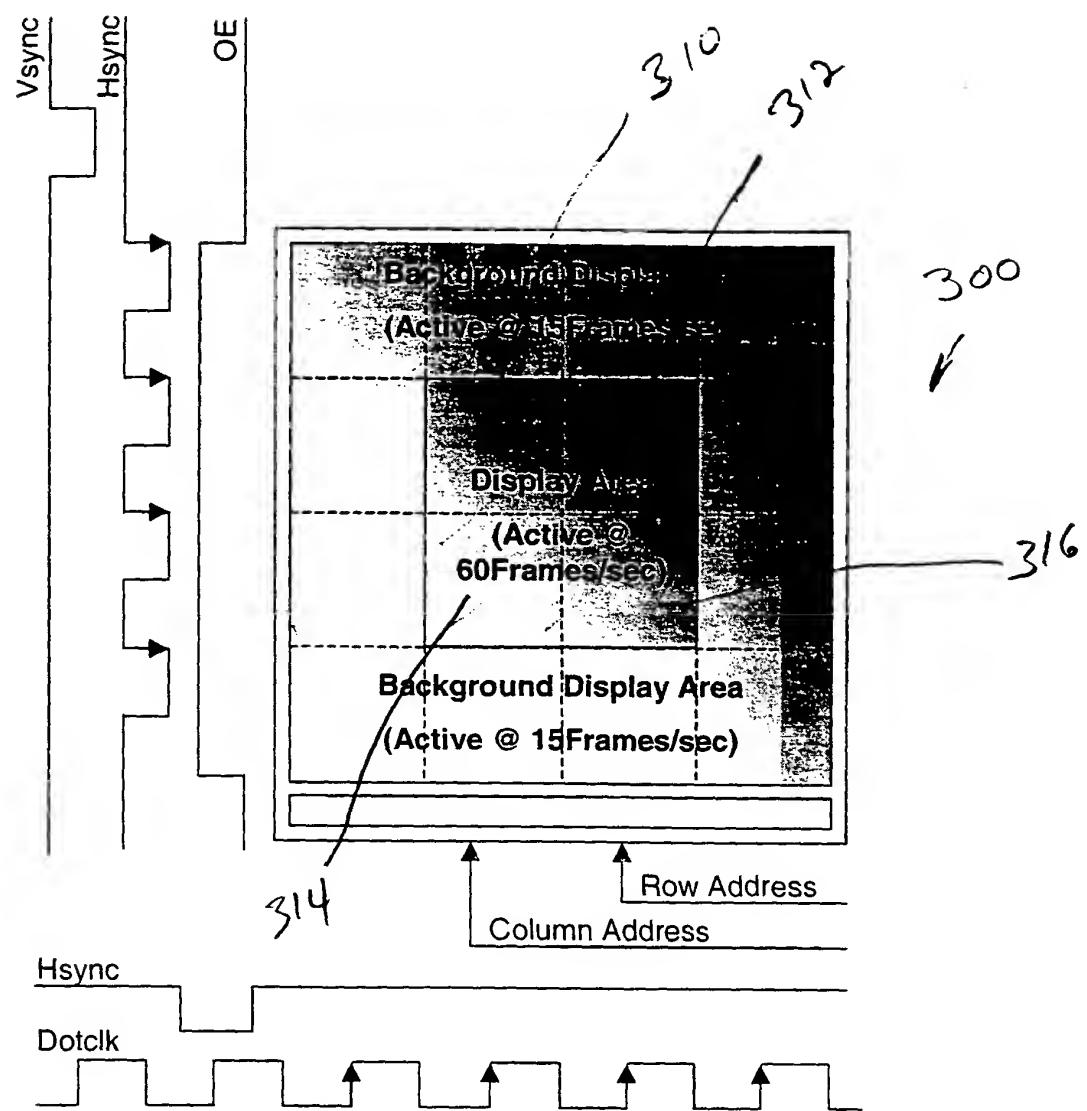


Fig 3

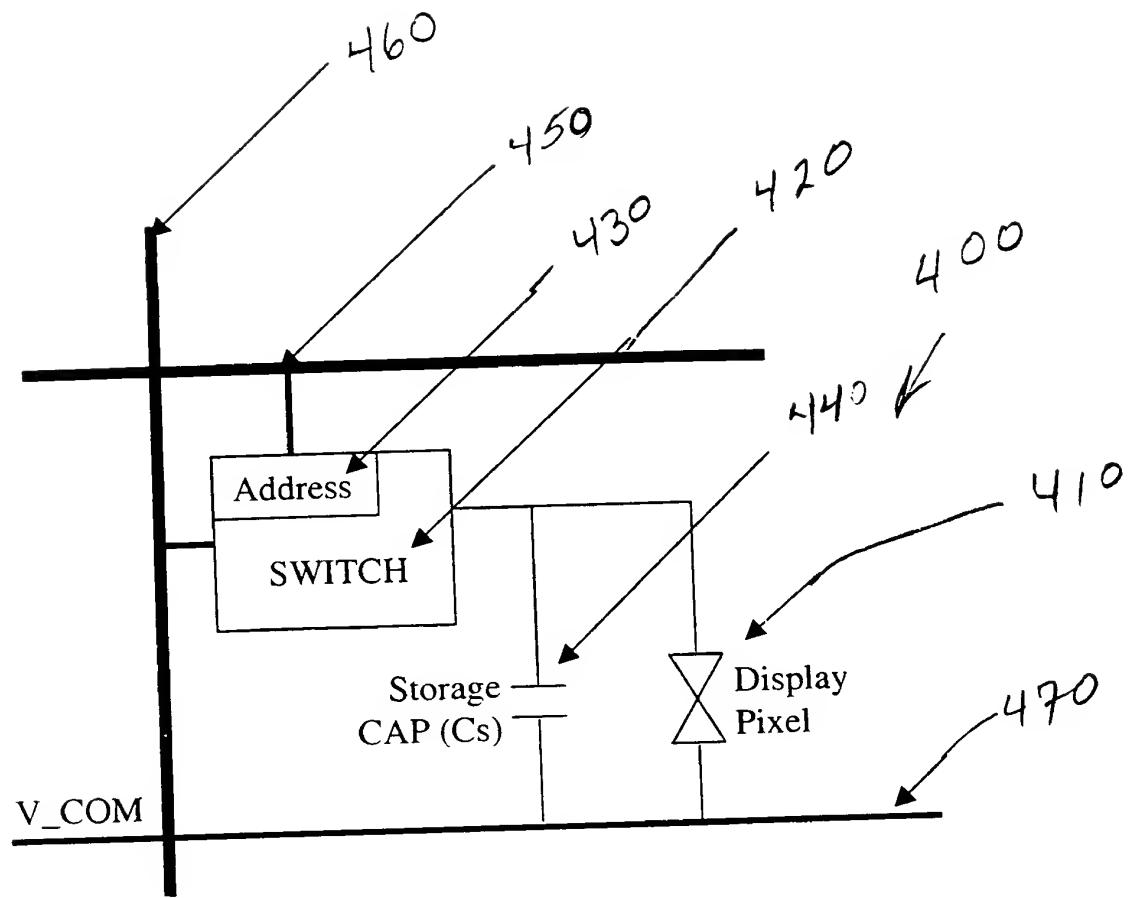


Fig 4

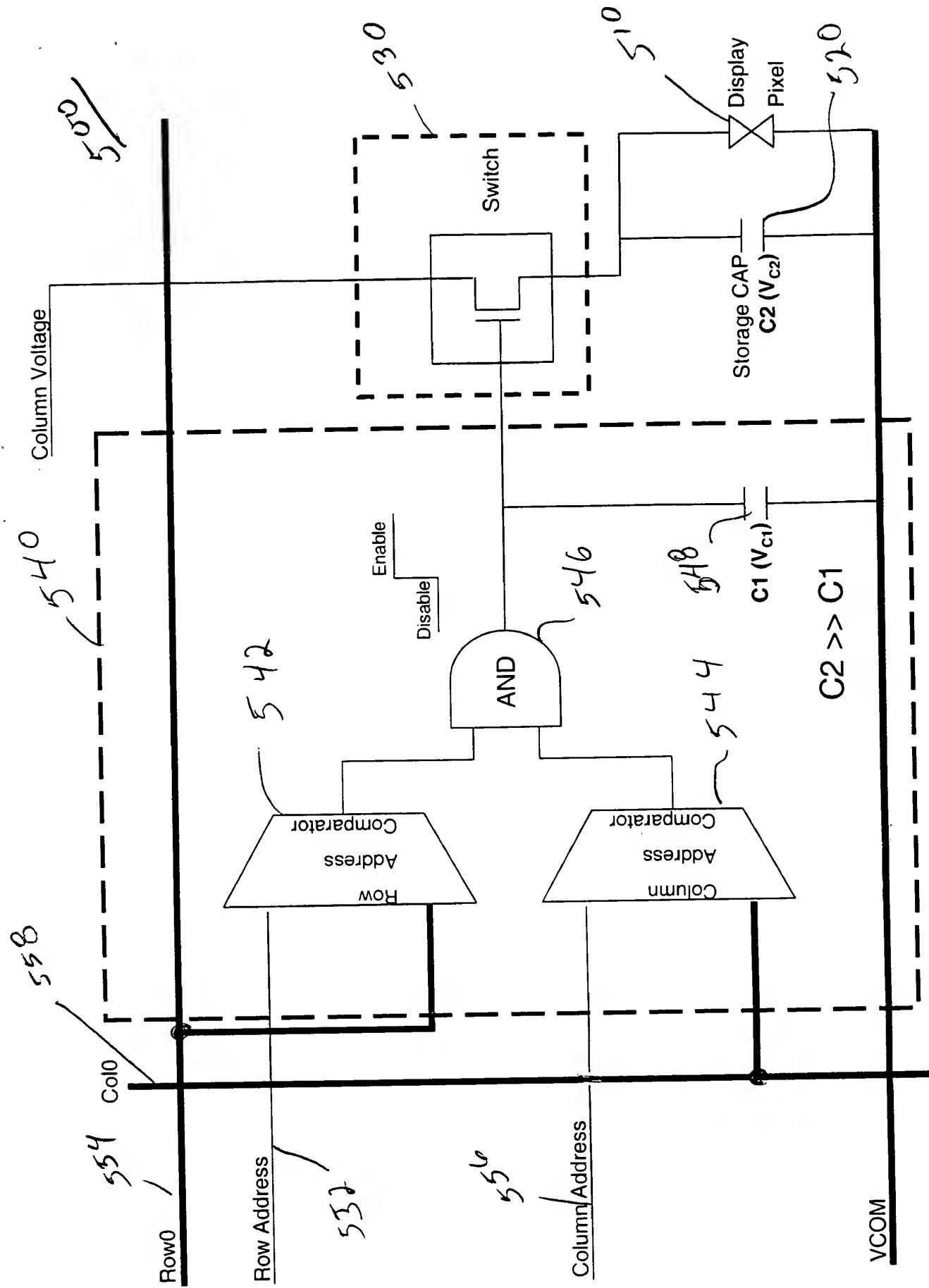


Fig 5

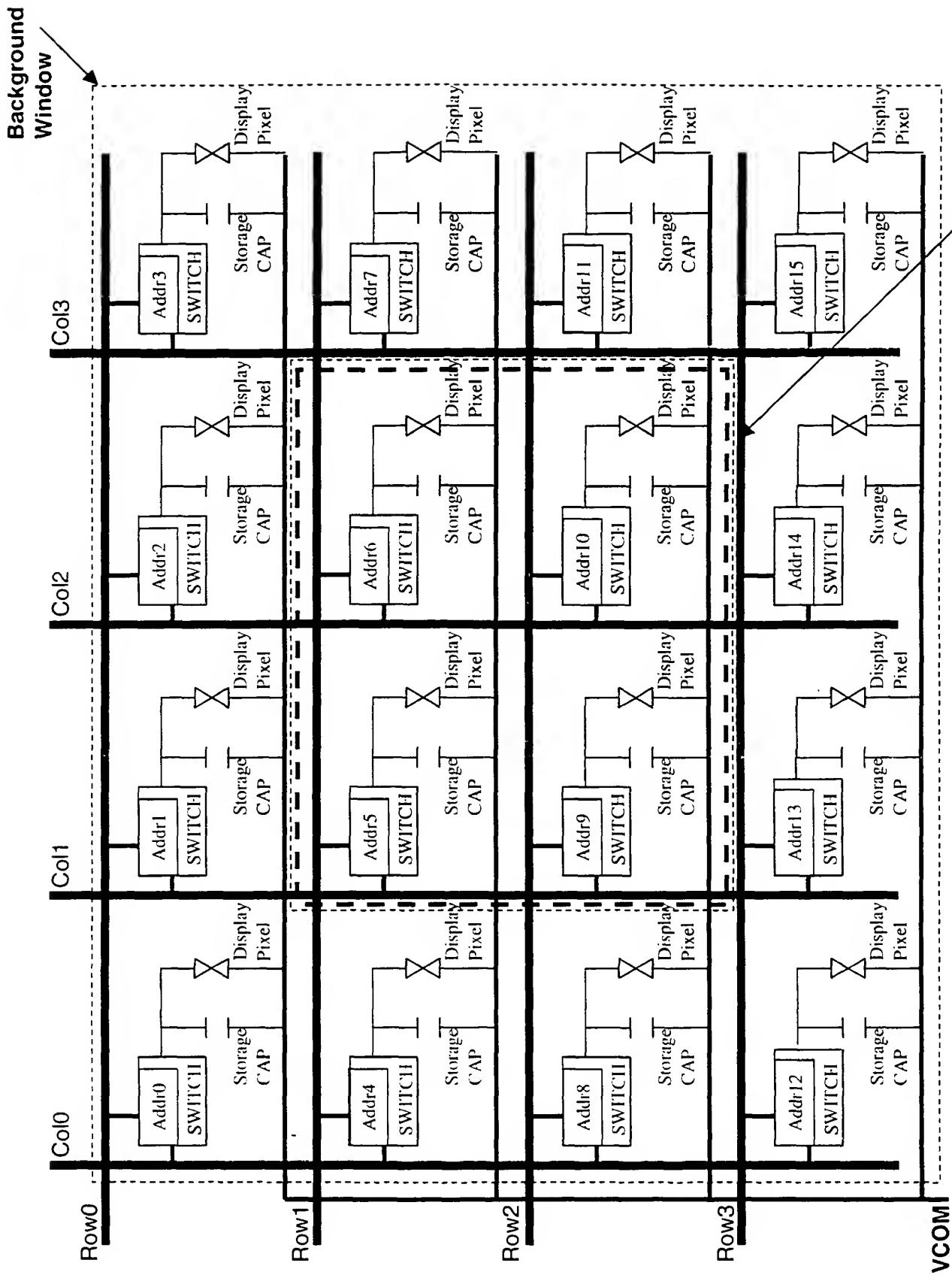


Fig 6 Pixel Array

Active Window

VCOM

Background Window

Fig 7

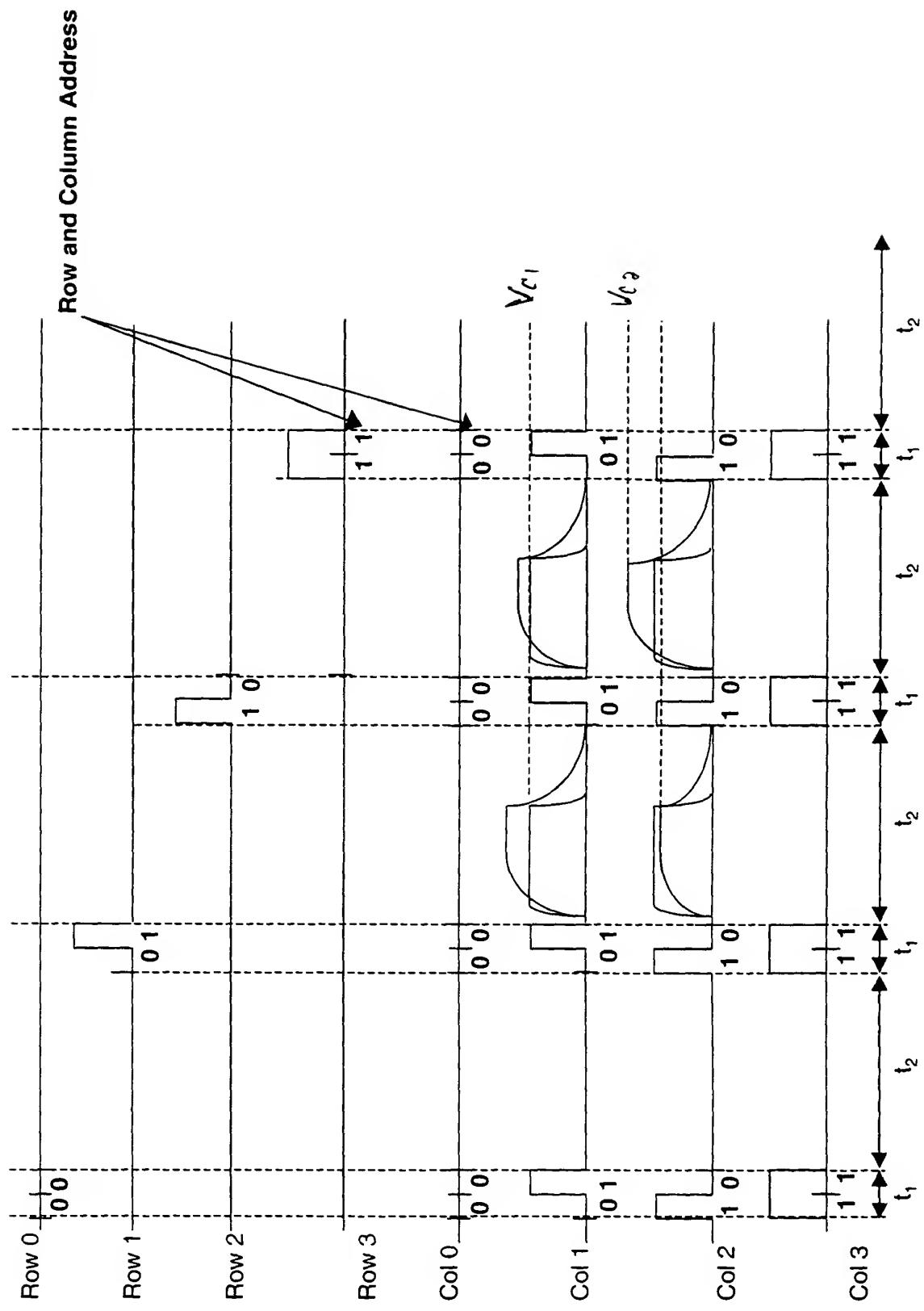


FIG 8

